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solution based on **variable interpolation** will be presented later on in Chapter 7. ... (6.12).

Figure 6.13. Scheme of the XNOR-based **complex multiplier**. ...

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DDS and the **complex multiplier** translate the signal spectrum from the baseband ... A **variable interpolator** allows the use of sampling rates which are not ...

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new error signal, which is employed to control a **variable interpolator** [Erup93], ... (6.12).

Figure 6.13. Scheme of the XNOR-based **complex multiplier**. ...

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The outputs from the **complex multiplier** 66 are in turn introduced to a **variable interpolator** 68. Output connections are respectively made from the **variable** ...

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3b, the **variable interpolator** 112 is disposed in front of the **complex multiplier** 118 in the progression of stages in the system. This is in contrast to the ...

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B. **Interpolator** and **Complex Multiplier**. In the prototype implementation, ... [11] C. Farrow, "A continuously **variable** digital delay element," in Proc. ...

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significant performance degradation. The **variable**. symbol-rate **interpolation** based on polyphase fil- ... consists of a **complex multiplier** which operates on ...

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EP802644 Nippon european software patent - **Variable**-bandwidth ...

A **variable**-bandwidth frequency-division multiplex communication system which needs no **interpolation** circuits, but uses switch element of simple structure. ...

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In an analog fader, the weights are applied to a **complex multiplier**. ... higher level **interpolation** could be used over a larger set of signal replicas. ...

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☐ 1. [ACOLADE CDMA Library \[PDF-89K\]](#)

Aug 1997

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IS-95-A Link Architecture.....


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☐ 2. [Multi-variable colorimetric data access by iterative interpolation and subdivision](#)

Gondek, Jay, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, May 1998

...herein is expandable for **interpolation** of any type of non-linear...and then to use a fast **interpolation** algorithm to approximate...technique not requiring **complex multiplier** and divider circuitry... n is the number of **variables** in the first data construct...tristimulus color space, a tri-**variable** data set, $n=3$. In this...value to be derived by **interpolation** (FIG. 6, step 603). For...

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☐ 3. [Variable-bandwidth frequency division multiplex communication system](#)

Ichiyoshi, Osamu, EUROPEAN PATENT, Oct 1997

...the present invention to provide a **variable**-bandwidth frequency-division multiplex...communication system which needs no **interpolation** circuits, but uses switch elements...frequency of $(i-1)\Delta f$ in an i -th **complex multiplier** 5. Output signals from the complex...signal from an i -th $(1 \leq i \leq N)$ **complex multiplier** 5 is supplied to the input terminal...

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☐ 4. [OFDM WITH VARIABLE SUBCARRIER ALLOCATION](#)

PIERZGA, Wayne, Francis / GUINAND, Paul, Scott / LODGE, John, Harrison / CROZIER, Stewart, Newman / YOUNG, Richard, John, EUROPEAN PATENT, Oct 1997

Re
us
fo
an
co
ce
de
fo
in
int
ou
ph
pr
qu
sig
sig
sp
ult
ult
wa
Or
Al

...two channels. The number of channels is **variable**, in use, by varying the number of subcarrier...The number of other subcarriers is **variable**, and each additional subcarrier occupies...EP). Since the number of channels is **variable**, this information is required by the...

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☐ 5. METHOD AND APPARATUS FOR A BASEBAND PROCESSOR OF A RECEIVE BEAMFORMER SYSTEM

WRIGHT, J., Nelson / COLE, Christopher R. / GEE, Albert, PATENT COOPERATION TREATY APPLICATION, Feb 1996


...stored in memory to be applied by a **complex multiplier** to the acquired coherent samples...response (FIR) filter and a programmable **complex multiplier**. In one aspect of the invention...processor includes a programmable **complex multiplier** which follows the programmable filter...

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☐ 6. System and method for digital tracking and compensation of frequency offset error in a satellite receiver

Ben-Efraim, Nadav / Keate, Christopher R., UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Dec 1998

...ADC) 220, a **complex multiplier** 522, an optional...block 524, an **interpolation** block 526...NCO) 534 by **complex multiplier** 522. NCO 534...output of **complex multiplier** 522 passes...device with a **variable** rate QPSK...

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☐ 7. Method and apparatus for receive beamformer system

Wright, J. Nelson / Cole, Christopher R. / Gee, Albert, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Oct 1998


...A. Mucci, "Digital **Interpolation** Beamforming for Low-Pass...example only, the **variable** time delay memory...which can include a **complex multiplier** but where at least...decimation, (2) a **variable** time delay memory...Hertz), and (4) a **complex multiplier** structure which can...

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☐ 8. Demodulating digital video broadcast signals

Guyot, Jean Marc / Clarke, Christopher Keith Perry / Mitchell, Justin David / Sadot, Philippe / Regis, Lauret / Stott, Jonathan Highton / Robinson, Adrian Paul / Haffenden, Oliver Paul, EUROPEAN PATENT APPLICATION, Nov 1998


A demodulator suitable for implementation in a single chip for demodulating digital video broadcast signals comprising data modulated on a multiplicity of spaced carrier frequencies, wherein an input broadcast signal is converted to a frequency ...

Full text available at patent office. For more in-depth searching go to  LexisNexis
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☐ 9. OFDM WITH VARIABLE SUBCARRIER ALLOCATION

PIERZGA, Wayne, Francis / GUINAND, Paul, Scott / LODGE, John, Harrison / CROZIER, Stewart, Newman / YOUNG, Richard, John, PATENT COOPERATION TREATY APPLICATION, Jun 1996

OFDM with **Variable** Subcarrier Allocation FIELD OF THE INVENTION This invention relates...to operate in synchronous mode. For example, "The ANIGSC-10 (KATHRYN)

Variable Rate Data Modem for HF Radio", Zimmerman & Kirsch; IEEE Trans. Commun...
Full text available at patent office. For more in-depth searching go to  LexisNexis-
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☐ 10. [Spread spectrum communication system](#)

Zhou, Changming / Shou, Guoliang / Zhou, Xuping / Suzuki, Kunihiro / Yamamoto, Makoto, EUROPEAN PATENT APPLICATION, Dec 1998


...transmission rates: In the DS-CDMA cellular system, a type of **variable** spread ratio and a parallel multi-code type are proposed. A...code register. The matched filters are of double sampling and **variable** in number of taps. The number is from 8 to 1024. Any matched...

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☐ 11. [OFDM active deflectors](#)

Stott, Jonathan Highton / Wells, Nicholas Dominic, EUROPEAN PATENT, May 1997

...filters in FIR form where the filter coefficients are dynamically **variable** in response to characteristics of the filter output. The coefficients...output of which is applied to a series circuit comprising a **variable**- gain amplifier 56, the gain of which can be controlled in...

Full text available at patent office. For more in-depth searching go to  LexisNexis-
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☐ 12. [Method and apparatus for receive beamformer system](#)

Wright, J. Nelson / Cole, Christopher R. / Gee, Albert, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Nov 1997


...A. Mucci, "Digital **Interpolation** Beamforming for Low-Pass...example only, the **variable** time delay memory...which can include a **complex multiplier** but where at least...decimation, (2) a **variable** time delay memory...Hertz), and (4) a **complex multiplier** structure which can...

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☐ 13. [METHOD AND APPARATUS FOR RECEIVE BEAMFORMER SYSTEM](#)

WRIGHT, J., Nelson / COLE, Christopher R. / GEE, Albert, PATENT COOPERATION TREATY APPLICATION, Feb 1996


...example only, the **variable** time delay memory...can include a **complex multiplier** but where at least...system includes the **complex multiplier** which applies...applied to the **variable** time delay memory...data from the **variable** time delay memory...embodiment of the **complex multiplier**, the phase and...

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☐ 14. [Reduced power tuner chip with integrated voltage regulator for a satellite receiver system](#)

Ben-Efraim, Nadav / Keate, Christopher, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Oct 1998

...error equalizer 466, a **complex multiplier** 468, a decimation block 470, an **interpolation** block 472, a matched...oscillator (NCO) 482 by **complex multiplier** 468. NCO 482 is set...signal at the output of **complex multiplier** 468 passes through...


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☐ 15. [System and method for correction of I/Q angular error in a satellite receiver](#)

Ben-Efraim, Nadav / Keate, Christopher, UNITED STATES PATENT AND TRADEMARK

OFFICE GRANTED PATENT, Sep 1998


...ADC) 220, a **complex multiplier** 522, an optional...block 524, an **interpolation** block 526...NCO) 534 by **complex multiplier** 522. NCO 534...output of **complex multiplier** 522 passes...device with a **variable** rate QPSK...

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☐ 16. Programmable modem using spread spectrum communication

Philips, Lieven / Vanhoof, Jan / Wouters, Maryse / De Wulf, Rick / Derudder, Veerle / Van Himbeek, Carl / Bolsens, Ivo / (...) / Gyselinckx, Bert, EUROPEAN PATENT APPLICATION, Apr 1997


...memory, an up-converter frequency memory, a transmitter modulation procedure selection memory, and an over-sampling filter **interpolation** factor memory for the transmitter. In one embodiment, the spreading codes and the spreading code lengths are PN codes and...

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☐ 17. SINGLE CHIP VLSI IMPLEMENTATION OF A DIGITAL RECEIVER EMPLOYING ORTHOGONAL FREQUENCY DIVISION MULTIPLEXING

ALAM, Dawood / COLLINS, Matthew, James / DAVIES, David, Huw / KEEVILL, Peter, Anthony / NOLAN, John, Matthew / FOXCROFT, Thomas / PARKER, Jonathan, PATENT COOPERATION TREATY APPLICATION, May 1998

The invention provides a single chip implementation of a digital receiver for multicarrier signals that are transmitted by orthogonal frequency division multiplexing. Improved channel estimation and correction circuitry are provided. The receiver has ...

Full text available at patent office. For more in-depth searching go to  LexisNexis
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☐ 18. Method and apparatus for coherent image formation

Wright, J. Nelson / Maslak, Samuel H. / Finger, David J. / Gee, Albert, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Aug 1998


...samples, can be calculated for multiple other azimuthal positions between R.sub.0-1 and R.sub.01 using various types of **interpolation** techniques as identified herein. Further using extrapolation techniques, synthetic samples can be generated outside of the...

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☐ 19. METHOD AND APPARATUS FOR CONTROLLING ENCODING RATE IN A COMMUNICATION SYSTEM

KOTZIN, Michael D. / STEWART, Kenneth A., EUROPEAN PATENT, Mar 1996

...IS-95 links exploit this **variable** rate encoding method. In...link performance. While **variable** rate transmission has little...speech decoder performing **interpolation** or frame substitution to...self-interference reduction by **variable** rate speech encoding is...preferred embodiment, of a **complex multiplier** and an integrator. Note...

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☐ 20. SYSTEM FOR DIVIDING PROCESSING TASKS INTO SIGNAL PROCESSOR AND DECISION-MAKING MICROPROCESSOR INTERFACING

ROBINSON, Jeffrey I. / ROUSE, Keith / KRASSOWSKI, Andrew J. / MONTLICK, Terry F., PATENT COOPERATION TREATY APPLICATION, Apr 1993

...constructs which can result in highly **variable** executdion times which axe readily

dealt...parameters of the functional blocks as **variables** into the functional block library code...table WE provides a nxmxqy map Which maps **variable** nanotes which the microprocessor will...

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